

METHOD FOR CONTROLLING AN IN- STRUCTION MEMORY OF AN EMBEDDED SYSTEM

Abstract

A method for controlling an instruction memory (IM) of an embedded system. The embedded system is electrically connected to a memory device used for storing a plurality of program code segments. The embedded system includes the IM and an execution unit. The steps of the method are setting up a look-up table for recording the operation status of the IM, and determining if a specific program code segment of the program code segments has been stored in the IM or not according to the look-up table when the execution unit selects the specific program code segment to execute. If the specific program code segment has been stored in the IM, the execution unit reads the specific program code segment from the IM. If not, the execution unit loads the specific program code segment from the memory device and executes it.